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PTO/SB/08A (08-00)

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 1

Complete if Known

Application Number	10/606,753
Filing Date	June 27, 2003
First Named Inventor	Robert B. MAGILL et al.
Group Art Unit	2661
Examiner Name	Unassigned
Attorney Docket Number	32172-188433

### OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
/BTO/	A1	CHUANG et al., "Matching Output Queueing With A Combined Input/Output-Queued Switch," IEEE Journal on Selected Areas In Communications, Vol. 17, No. 6, pp. 1030-1039, (June, 1999).	
/BTO/	A2	STOICA et al., "Exact Emulation of an Output Queueing Switch By a Combined Input Output Queueing Switch," presented at 6 <sup>th</sup> IEEE/IFIP IWQoS, pp. 218-224, (1998).	
/BTO/	A3	ROJAS-CESSA et al., "CIXB-1: Combined Input-One-Cell Crosspoint Buffered Switch," Proc. 2001 IEEE Workshop on High Performance Switching and Routing, pp. 324-329, (May, 2001).	
/BTO/	A4	ROJAS-CESSA et al., "CIXOB-k: Combined Input-Crosspoint-Output Buffered Packet Switch," Proc. IEEE Globecom 2001, pp. 2654-2660, (December, 2001).	
/BTO/	A5	ELHANAY et al., "A Contention-Free Tbit/s Packet-Switching Architecture For ATM Over WDM Networks," IEICE Trans. Commun., Vol. E83-B, No. 2, pp. 225-230, (Feb. 2000).	
/BTO/	A6	CHAO et al., "Delay-Bound Guarantee in Combined Input-Output Buffered Switches," Proc. IEEE GLOBECOM 2000, pp. 515-524, (November, 2000).	

Examiner  
Signature

/Brian O'Connor/ (03/13/2007)

Date  
Considered

03/13/2007

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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**Not Yet Assigned**

Attorney Docket Number

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## U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			

#### OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

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